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From-MOTOROLA

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MOTOROLA

FAX TRANSMITTAL SHEET

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4 Number of Pages (including this page)

Date: November 18, 2003
To: **OFFICE OF OFFICIAL PATENT EXAMINATION'S
FILING RECEIPT CORRECTIONS**
Location: United States Patent and Trademark Office
Fax No.: (703) 746-9195
From: Kenneth A. Haas 42,614

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MESSAGE:

Enclosed herewith, please find a Request to Correct Filing Receipt for filing in the below-identified application.

GROUP ART UNIT:	2818
SERIAL NO.:	10/646,231
FILED:	8/22/2003
INVENTOR:	Brazis et al
DOCKET NO.:	CML01198T

PATENT
CML01198T

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Brazis et al
SERIAL NO.: 10/646,231
FILE DATE: 8/22/2003
GROUP ART UNIT: 2818
TITLE: METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY

CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: Office of Initial Patent Examination, Customer Service Center, COMMISSIONER FOR PATENTS, ALEXANDRIA, VA 22313, ON: 11/18/2003

Date of Deposit

MOTOROLA INC.
Name of Assignee

Dawn Littlehorn
SIGNATURE

11/18/2003

DATE

CORRECTION OF FILING RECEIPT

Office of Initial Patent Examination
Customer Service Center
Commissioner for Patents
Alexandria, VA 22313

Sir:

It is respectfully pointed out that the Filing Receipt for the above-identified patent application is incorrect. The title of the application is incorrect. "METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY" should be METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY".

Please correct the filing receipt to include the corrected title as follows METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY". A copy of Page One of the Specification is attached along with a copy of the Filing Receipt with the change noted thereon.

No fee is due since the error was not that of Applicant.

Respectfully submitted,



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10/646,231	08/22/2003	2818	750	CML01198T	5	15	3

22917
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NOV 18 2003

PATENT DEPT
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METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY

Field of the Invention

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The present invention relates generally to solid-state memory and in particular, to a method and apparatus for reading and writing to solid-state memory.

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Background of the Invention

Large-scale (>1GB) solid-state memory storage is a rapidly expanding market, particularly for multimedia applications. Currently these storage devices have not been successfully applied in usage scenarios where large storage capabilities are needed. For example, personal computers still utilize hard-disk storage as a primary storage mechanism. In order for manufacturers to utilize solid-state memory devices in place of hard-disk storage and for high-reliability applications, the performance of such solid-state memory devices must be improved. One way to improve performance of solid-state memory storage is to increase the performance of read-write operations so that such operations occur more efficiently, and data are well protected from device failures.

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Brief Description of the Drawings

FIG. 1 is a block diagram of solid-state storage means.

FIG. 2 is a flow chart showing operation of the solid-state storage means of FIG. 1.

FIG. 3 is a flow chart showing a method for detection of non-operational die and the actions of a controller in such a situation.

FIG. 4 is a flow chart for dynamically updating a performance model database.

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